

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7006	345/156,530,204.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:03
L2	622	1 and (((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash) and (time temporal) and (space spatial))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:23
L3	168	2 and (zoom scroll)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:18
L4	2	1 and Penna.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:20
L5	55	1 and smith.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:21
L6	0	1 and miller-mith.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:21
L7	3	1 and miller-smith.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:21
L8	0	1 and (((control data) adj input) near (logic circuit\$2) near (memory stor\$3 record\$3 tag cache RAM (random access memory) flash) near (time temporal) and (space spatial))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:24

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L9	3	1 and (((control data) adj input) near (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash) near (time temporal) and (space spatial)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:25
L10	15	1 and (((control data) adj input) near5 (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash) near (time temporal) and (space spatial)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:25
L11	16	1 and ((portable pda cellular cellphone mobile) and ((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash) and (time temporal) and (space spatial) and camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:46
L12	304	1 and ((portable pda cellular cellphone mobile) and ((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash cpu processor) and (time temporal) and (space spatial)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 12:50
L13	16	12 and (camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:58
L14	9	13 and (scroll zoom)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 11:58
L15	363	1 and ((portable pda cellular cellphone mobile) and ((control data) adj input) and (memory stor\$3 record\$3 tag cache RAM (random access memory) flash cpu processor) and (time temporal) and (space spatial)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 12:13

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L16	16	13 and 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 12:13
L17	4	1 and (((portable pda cellular cellphone mobile) and ((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu processor) and (time near mode) and (location (space near mode))))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 12:57
L18	12	1 and (((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu processor) and (time near mode) and (location (space near mode))))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 12:59
L19	3	1 and (((control data) adj input) and (logic circuit\$2) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu processor) and (time near mode) and (location (space near mode)) and camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 13:04
L20	3	1 and ((data input) and (logic circuit\$2 control) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu process\$3) and (time near mode) and (location (space near mode)) and camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 13:20
L21	3	1 and ((logic circuit\$2 control) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu process\$3) and (time near mode) and (location (space near mode)) and camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 13:22
L22	99	1 and ((logic circuit\$2 control) and (memory stor\$3 record\$3 tag cache RAM (random adj access adj memory) flash cpu process\$3) and (time and (location space)) and camera and gps)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	ON	2007/05/01 13:22